

Zagdu Singh Charitable 'Trust's (Regd.) THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE, Govt. of Maharashtra & Affiliated to University of Mumbai*) (Accredited Programmes by National Board of Accreditation, New Delhi**)

A - Block, Thakur Educational Campus, Shyamnarayan Thakur Marg, Thakur Village, Kandivali (East), Mumbai - 400 101.

Tel.: 6730 8000 / 8106 / 8107 Fax: 2846 1890 Email: tcet@thakureducation.org
Website: www.tcetmumbai.in • www.thakureducation.org



ISO 9001:2008 Certified

Revision: B

*Permanent Affiliated UG Programmes: • Computer Engineering • Electronics & Telecommunication Engineering • Information Technology (w.e.f.: A.Y. 2015-16 onwards)
**1st time Accredited UG Programmes: • Computer Engineering • Electronics & Telecommunication Engineering • Information Technology

TCET/FRM/IP-02/10

Semester Plan (Practical / Tutorials / Assignment)

Semester: V Course: **EXTC** Batches: T.E (B1B2)

Subject: Mini project I Class: T.E EXTC- B Batch size: 20 Students

Laboratory faculty in charge: Mr. MANOJ CHAVAN Lab. Assistant /Attendant: Ms. Asmita Parab

(Lab 103)

Note: Experiment planned as per University Curriculum

Basic Experiments:

Sr. No.	TITLES Experiments / Tutorials / Assignment (Planning with use of Technology)	Planned Date B1-B2	Completion Date	Remarks
1.	To search and select the project title	28/7/17		
2.	To prepare synopsis and presentation of selected project	4/8/17		
3.	To present project idea for selected project title	11/8/17		
4.	Implementation of circuit on breadboard /study of software for code writing /learning the simulation	18/8/17		

Design/ Development Experiments:

5.	Testing of circuit / code writing and testing	1/9/17	
6.	Progress presentation	8/9/17	

Issued By: MR Approved By: Principal



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	**2nd time Accredite	ed IIG Programmes •• Computer Eng	ineering • Flectronics & Teler	communication Engineeri	ng • Information Technology • Flectronics Engineer	ing (3 years w		
7.		lementation/	-	-	15/9/17			
8.	PCB imp	lementation/	Simulation	n results	22/9/17			
Group	Group Learning Activity:							
9	Demo pro	Demo presentation and viva			6/10/17			
10	Submissi	on of final re	rototype	13/10/17				
					pect to prerequisites mester / student)	and	industry ski	lls or to
1. The a	Mini /Mine respect to (Total 120 reas are: 1. Rese	or Projects Of student choice	bjective: T e in the follo	o get hand	ds on experience to eas. (30 Hrs / Semes	execu t er / S	te projects Student).	
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Prac	Basic Exp: 04 Design Base Exp: 04 Group Learnin g: 02		tation	02		of R e p o rt s	02	
	Major Project: 02							

DOSLNE: Group activities are required to be added with the practical related to course to enhance the learning activity of the student in the course. Group activity includes: Group presentation, new experiment design, mini projects etc. Note: 1. The practical plan date and completion date shall be in compliance. For any non-compliance reason(s) required to be stated in remark column. 2. Learning objective and outcome shall be clearly stated with each of experiments/ tutorials/ assignments and are required to be mapped at the end of the semester. 3. Entry for DOSLE (engaged on some other date) shall be done with proper mapping to DOSLNE. (Mr. Manoj S. Chavan) Name & Signature of Faculty Signature of HOD Signature of Principal / Dean Academic Date: Date: Date: Date: Date: Date: Date: Issued By: MR Approved By: Principal	02						
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**21st time Accredited UG Programmes: "Computer Engineering - Electronics & Telecommunication Engineering - Information Technology & Electronics Engineering (3 years w.e.f.:01-07-2016)

TCET/FRM/IP-02/10 Revision: B

Semester Plan (Practical / Tutorials / Assignment)

Course: **B.E EXTC** Semester: V Batches: T.E (B3B4)

Subject: Mini project-l Class: T.E EXTC- B Batch size: 40 Students

Laboratory faculty in charge: Ms.Purnima ChandrashekharLab. Assistant /Attendant:

(Lab Attendant 103)

Note: Experiment planned as per University Curriculum						
Basic	Experiments:					
Sr. No.	TITLES Experiments / Tutorials / Assignment (Planning with use of Technology)	Planned Date	Completion Date	Remarks		
1.	To search and select the project title	27/07/17				
2.	To prepare synopsis and presentation of selected project	03/08/17				
3.	To present project idea for selected project title	10/08/17				
4.	Implementation of circuit on breadboard /study of software for code writing /learning the simulation	24/08/17				
Design/ Development Experiments:						
5.	Testing of circuit / code writing and testing	24/08/17				
6.	Progress presentation	31/08/17				

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**2nd time Accredited UG Programmes: *Computer Engineering *Electronics & Telecommunication Engineering *Information Technology

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**Information Technology *Electronics Engineering (3 years w.e.f::01-07-2016)

7.	PCB implementation/ Stresults	07/09/17						
8.	PCB implementation/ S results	07/09/17						
Group Learning Activity:								
9	Demo presentation and	14/09/17						
10	Submission of final repprototype	21/09/17						
The a	Mini /Minor Projects Objective: To get hands on experience to execute projects with respect to student choice in the following areas. (30 Hrs / Semester / Student). (Total 120 Hrs) The areas are: 1. Research 2. Core 3. Interdisciplinary 4. Application Major project: As per University Scheme							
S.No	Project Title/Group Size	Class	Type / Project Hours	Modes of Learning	Reference			
S.No 1.		Class B.E EXTC-A	Project		Reference http://www.mycollegeproject.com/Abstracts/SEM-53.%20Talking%20energy%20meter.pdfhttp://ijiet.com/wp-content/uploads/2014/05/29.pdf			

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	Planned	Complete d		Plan ned	Completed		Planned	Completed
No. of Prac	Basic Exp: 04 Design Base Exp: 04 Group Learnin g: 02 Bridge Course: 02 Major Project: 02		No. of Presen tation	02		No. of Reports	02	

DOSLNE: DOSLE (engaged in some other dates):

Group activities are required to be added with the practical related to course to enhance the learning activity of the student in the course. Group activity includes: Group presentation, new experiment design, mini projects etc.

- 1. The practical plan date and completion date shall be in compliance. For any non-compliance reason(s) required to be stated in remark column.
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SD	SD	SD
(Mr.Nikhil Tiwari)	(Dr. Vinitkumar Dongre)	(Dr. R. R. Sedamkar)
Name & Signature of Faculty	Signature of HOD	Signature of Principal / Dean Academic

Date: 20 /07/2017 Date:20 /07/2017 Date:20 /07/2017

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